# of Nonlinear Optical IFWM Impairments in Optical Fiber by Modeling 40 Gb/s Transimpedance Amplifier

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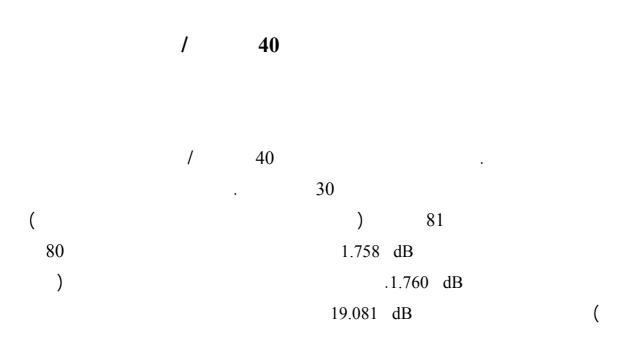
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#### ABSTRACT

A suppression of nonlinear optical Intra-Channel Four Wave Mixing (IFWM) induced phase and amplitude noise in optical fiber is reported by simulation. A 40 Gb/s transimpedance amplifier (TIA) circuit was investigated for this purpose. Circuit components were modeled under thermal conditions of 30°C. Using 81 k $\Omega$  nonlinear feedback resistor R<sub>TIA</sub>, an optimal Minimum Noise Figure (MNF) of 1.758 dB was achieved. A 1.760 dB of MNF is reported when R<sub>TIA</sub> value changes to 80 k $\Omega$ . However, when an IFWM-induced phase and amplitude combined noise is applied at the TIA input with a magnitude of 19.081 dB, it was effectively suppressed under the influence of TIA input impedance Z<sub>in</sub> of 19914  $\Omega$  which lead to IFWM combined noise of 14.55 dB in a decreasing pattern at TIA output. An  $|S_{21}|$  simulated measurement showed a gain of 10.15 dB.

Keywords: IFWM, 40 Gb/s TIA, Nonlinear Optics, Noise Suppression.



14.55 dB 19914

.10.15 dB  $|S_{21}|$ 

# **INTRODUCTION**

Optical fiber transmission systems are subject to impairments by Intra-Channel Four-Wave Mixing (IFWM), a form of nonlinear Inter-Symbol Interference (ISI) resulting from the interaction of Kerr nonlinearity and chromatic dispersion (Lau et al., 2008). Nonlinear effects impair the transmitted signal severely over high-speed optical fiber channel, and hence became the main obstacle for long-distance and high quality signal transmission. Research activities for dealing with nonlinearity mainly were dispersion management technologies (Kumar et al., 2002) (Yamamato et al., 2000) (Morita et al., 1999), or amplification technologies (Hirooka and Ablowitz, 2002) depending on the loss compensation and signal power amplification. Suppression of IFWM nonlinear effects was achieved using optical pulse position modulation (Linghao and Takala, 2003).

A phase-modulated pulse trains in a single-channel system can be transmitted into a fiber. The signal at the transmitter is given by (Lau et al., 2008).

$$E(0,t) = \sum_{k} x_{k} u(0,t-kT) = \sum_{k} x_{k} u_{k} \qquad ..... (1)$$

where  $x_k = \{e^{j2\pi/M}, e^{j4\pi/M}, \dots, e^{j2\pi}\}$  are the *M*-ary phase-modulated signals, u(z,t) is the pulse shape, and T is the symbol period. Signal propagation in optical fiber is described by the nonlinear Schrodinger Equ (Agrawal, 2002):

 $\beta_2$  is the chromatic dispersion parameter and  $\gamma$  which is the nonlinear coefficient. The firstorder perturbation theory can be used to approximate the solution of the nonlinear Schrodinger Equ, in which the transmitted signal can be decomposed (Mecozzi, et al 2000):

Where  $E^{(l)}$  is the linear solution of Equation 2, while  $\Delta E$  is the nonlinear perturbation. The perturbation term satisfies (Lau et al., 2008):

$$\frac{\partial \Delta E}{\partial z} - \frac{j\beta_2(z)}{2} \frac{\partial^2 \Delta E}{\partial t^2} + \frac{\alpha(z)}{2} \Delta E$$
  
=  $j\gamma(z) |E^{-1}|^2 E^{-1} = j\gamma(z) \sum_{l,m,p} x_l x_m x_p^* u_l^{-1} u_m^{-1} u_p^{-1}$  .....(4)

With high local dispersion, the terms in the right hand side of Equation 4 that contribute to  $\Delta u_k$  are those with l + m - p = k and therefore, the nonlinear signal perturbations are termed IFWM. The nonlinear perturbation to a pulse  $u_0^{(l)}$  originate from Equation 4 is:

$$j\gamma \sum_{l,m} x_l x_m x_{l+m}^* u_l^{(l)} u_m^{(l)} u_{l+m}^{(l)*}$$

 $C_{l,m}$  can be assumed to be the solution to by linearity [Lau, *et al* 2008]:

The overall nonlinear perturbations is then  $\Delta u_0 = \sum_{l,m} C'_{l,m}(t)$ . If  $u_0^{(s+x)}$  is denoted as the collective nonlinear deterministic perturbations, the IFWM-induced phase noise can be approximated as [Lau, *et al* 2008]:

$$\varphi_0(t) \approx \sum_{l,m\neq 0} \operatorname{Im}\left\{\frac{x_l x_m x_{l+m}^* C_{l,m}'(t)}{x_0 \left(u_0^l + u_0^{(S+X)}\right)}\right\} = \sum_{l,m\neq 0} \operatorname{Im}\left\{x_l x_m x_{l+m}^* x_0^* C_{l,m}\right\} \dots \dots \dots (6)$$

And the IFWM-induced amplitude noise normalized with respect to the signal amplitude is:

So, the correlation between IFWM-induced phase and amplitude noises is given by:

in which  $A = x_l x_m x_{l+m}^* x_0^*$ .

A 40 Gb/s transimpedance amplifier (TIA) has not yet been considered for suppression of IFWM-induced noise (ghost pulses) accompanying main optical pulse and exiting the fiber. In fact, TIA configuration has never been considered before for processing IFWMinduced noise. That is because noise cancellation techniques were not advanced enough to deal with this problem. The importance of IFWM suppression becomes clear at high data bit rates alongside other nonlinear optical impairments such as self phase modulation and cross talk that need to be suppressed in an optical fiber. The transformation from optical domain (IFWM-induced noise) in an optical fiber to an ac electrical current (electronic domain) does happen when an optical pulse is incident on a photodiode and an electrical current is generated at the input of transimpedance amplifier (TIA). The TIA then converts this electrical current into amplified voltage swing at its output. A Microwave Office program was used to simulate the operation of TIA circuit.

Nonlinear effects impair the transmitted signal severely over high-speed optical fiber channel, and hence became the main obstacle for long-distance and high-quality signal transmission (Linghao and Takala, 2003).

# **RESULTS AND DISCUSSION**

# 1. TIA Design Analysis

#### A. Transfer Function

The transfer function of basic schematic of Fig 1 was envisaged according to similar concept stated in already published result (Groenveld, 2010). Although that, in this work, a capacitor C in parallel with feedback "nonlinear" resistor  $R_{TIA}$  is taken into account when transfer function (transimpedance gain) is worked out and it is given as follows:

 $\frac{V_{out}}{i_{in}} = \frac{As + B}{as^2 + bs + c} \quad \dots \tag{9}$ 

where,

$$\begin{split} A &= Z_{L}R_{TIA}C(g_{m}Z_{in}-1) \\ B &= Z_{L}[g_{m}(Z_{in}+R_{TIA})-1] \\ a &= Z_{in}R_{TIA}C_{in}C \\ b &= R_{TIA}C(g_{m}Z_{L}+1)+C_{in}(Z_{in}+R_{TIA}) \\ c &= g_{m}Z_{L}+1 \end{split}$$

 $g_m$  is the transistor transconductance measured in Siemens. A zero pole was deduced from Equ 9 as indicated:

$$s = -\frac{g_{\rm m}(Z_{\rm in} + R_{\rm TIA}) - 1}{R_{\rm TIA}C(g_{\rm m}Z_{\rm in} - 1)} \qquad (10)$$

in which a calculated value showed that  $s = -20.78 \times 10^9$  as  $s = j\omega$  where  $g_m$  is the trasnconductance of the common-source configured Angelov (HEMT) Transistor M<sub>1</sub> model (Angelov, 1996). C<sub>in</sub> is the equivalent input capacitance of TIA as it is within Twin T notch filter at the input. Load impedance  $Z_L = R_L / (R_L C_L s + 1)$  and was found to be  $|Z_L| = 110.2 k\Omega$ , while  $Z_{in} = 19914 \Omega$  at 40 GHz (measured in simulation) as the input impedance of the TIA and it is the Twin T notch filter total impedance.  $R_{TIA}$  was set at a range of 80 k $\Omega$  - 81 k $\Omega$ . The equivalent passive circuit of  $R_{TIA}$  is given in Fig 2 for which the  $R_{TIA}$  IV characteristics are illustrated in Equation 11. The IV characteristics are only for the nonlinear resistor  $R_{TIA}$  given in the Microwave Office program. The  $R_{TIA}$  is assumed by the program to be nonlinear that follows Equation 11, while the two diodes are reverse biased to isolate the earth connection from the actual  $R_{TIA}$  resistor.

where P and  $P_1$  are vectors of polynomial coefficients which were set at 1. Setting these vectors at 1 is to have the  $R_{TIA}$  resistor behaves in a Taylor Series configuration.

In addition to the zero pole, two complex poles were established using the formula  $s = [(-b \pm \text{sqrt}(b^2 - 4ac) / 2a] \text{ of the denominator of Equation 9, in which calculated imaginary$ 

values are  $s = -3 \times 10^{6}$  and  $-9.84 \times 10^{9}$ . A transimpedance gain of 40.82 dB $\Omega$  is obtained, once a 100  $\mu$ A is applied as TIA input, by which a voltage swing of 0.011V is achieved at the output. The TIA electrical gain may be considered moderate at first glance, however, given the thermal conditions of 30°C for main circuit components (*i.e* resistors and transistor models) as thermal sources, it may have reached its maximum possible value.

#### **B.** Noise Cancellation

A noise cancellation process takes place in a virtual ground node between Z<sub>in</sub> and (R<sub>TIA</sub>  $\parallel 1/Cs$ ) in which a feedback resistor is effectively in place,  $R_F = Z_{in} + (R_{TIA} \parallel 1/Cs)$ . This virtual ground node has two functions. The first is to thermally cancel noise and is located between R<sub>TIA</sub> and Z<sub>in</sub>, while the other one is located between (1/Cs) and Z<sub>in</sub> which is responsible for cancelling (*i.e* suppressing) IFWM combined noise of phase and amplitude. The value of  $R_F$  was calculated (frequency domain) and found to equal 20.5 k $\Omega$ . According to already published result with small signal model (Groenveld, 2010), a noise subtraction does happen in which the input referred noise of TIA corresponds to a value of -  $R_{TIA}$  as the minus sign indicates that by increasing R<sub>TIA</sub>, the input referred noise is decreased. That concept may be true in terms of thermal noise cancellation, however, as it will be seen in IFWM-Induced Noise Section that IFWM-induced noise is not affected by R<sub>TIA</sub> value, instead it will be influenced by TIA input impedance Z<sub>in</sub> and that is mainly true due to the virtual ground node between (1/Cs) and  $Z_{in}$ . Capacitances  $C_2$ ,  $C_3$  and  $C_4$  in Fig 1 could have been reduced even further to fF level, however, it becomes at the expense of overall circuit gain and may not necessarily maintain close matching between noise bandwidth (IFWM in this case) and TIA bandwidth. Hence, the closest matching possible is shown in Table 1.

Value	This Work	Published Work (Groenveld, 2010)
ZL	$ Z_{\rm L}  = 110.2 \ \rm k\Omega$	-
	$R_{\rm F} = 20.5 \ \rm k\Omega$	-
	$1/g_m = 20 \text{ k}\Omega$	-
	$Z_L > R_F$	$Z_L >> RF$
	$Z_L > 1/g_m$	$Z_L >> 1/g_m$
Z <sub>in</sub>	19914 $\Omega$ Twin T Notch	R <sub>F1</sub> Resistor
Rout of Transistor M2	$L_{1s} \parallel (1/C_{1s}) = 329 \ \Omega$	$102 \Omega$ Resistor
R <sub>TIA</sub>	80 kΩ - 81 kΩ	R <sub>F2</sub> (asymmetric)
	Nonlinear Resistor	Resistor
Data Rate	40 Gb/s	5.2 Gb/s
TIA Bandwidth	$(g_m Z_L + 1)/C_{in}(R_F + Z_L)$	$1/(Z_{in} C_{in})$
IFWM Noise	$(g_m Z_L + 1)/C_{in} [(Z_{in}    (1/Cs) + Z_L)]$	Not Related
Bandwidth		
Transistor M <sub>1</sub>	Angelov (HEMT) (Angelov, 1996)	NMOS
Transistor M <sub>2</sub>	Statz (GaAs FET) (Statz, 1987)	PMOS
Group Delay (GD)	0.00098 ns	-
Operating Temp.	30°C	-

Table 1: comparison with already published work (Groenveld, 2010)

The TIA circuit stability gives a good indication into how functioning is the noise cancellation process. A voltage controlled current source Statz transistor M<sub>2</sub> model (Statz, 1987) is embedded with (L<sub>1</sub> $s \parallel (1/C_1 s)$ ) load that determines how stable is the TIA. Fig 3 shows that the electronic circuit is conditionally stable (K < 1) at 40 Gb/s with a strict condition that the real part of Z<sub>21</sub> parameter is actually below zero which happened to be the case of -6.37 dB.

The idea that the feedback resistor thermal noise dominates the front end noise, and hence the electrical sensitivity being proportional to  $1/\sqrt{R_{TIA}}$  can be compromised due to the virtual ground node between  $R_{TIA}$  and  $Z_{in}$ , especially for a moderately large value of  $R_{TIA}$ . Therefore, a form of adaptive nonlinear feedback resistor is exactly what applies to  $R_{TIA}$  in which its dynamic range is extended to make it adapt to the input signal strength. Without the virtual ground node, a shunt resistor can be connected in parallel with  $R_{TIA}$  in order to extend the TIA dynamic range (adaptive transimpedance), however, this process may not be useful given the need for noise cancellation (Sackinger, 2005).

#### C. Design Comparison

A comparison in TIA design with already published work (Groenveld, 2010) is illustrated in Table I. There are major differences that involve design consideration at no parasitic capacitance (at this stage). In this work, a twin T notch filter, values of  $Z_L$  in relation to  $R_F$  and  $1/g_m$ , TIA bandwidth, types of high-speed transistors, Statz transistor model load and nonlinear resistor form are all exclusively set up. Angelov Transistor  $M_1$  transconductance  $g_m$  was calculated and found to have the magnitude of around 5 x 10<sup>-5</sup> S, in which  $1/g_m$  value is given in Table I. The Angelov model do replace the NMOS transistor, while the Statz model replaces the PMOS transistor. They are by no means equivalent to NMOS and PMOS transistors respectively.

Using the normalized value of  $\omega/\omega_n$ , for which  $\omega_n$  is the frequency of operation (40 GHz), while  $\omega$  is the sweeping frequency (*i.e* from 1 GHz up to 40 GHz), the TIA group delay (GD) is shown in Fig 4. The lowest GD value does occur at 40 Gb/s. However, data rates as low as 0.2 on a scale of  $\omega/\omega_n$  would also exhibit low GD values, and that corresponds to the conditionally stable terms as in Fig 3. An input overload current of TIA is inversely proportional to the total absolute value of the feedback resistor even if it is of a nonlinear form. Having  $R_{TIA} = 81 \text{ k}\Omega$  puts the input current of 100  $\mu$ A within an acceptable range.

# 2. IFWM-Induced Noise Suppression

Before considering IFWM-induced noise, thermal characterization of the TIA amplifier was evaluated. Main thermal sources are "resistors" as well as Angelov and Statz transistor models. These components were set in simulated operation at 30°C. Parastic capacitances of the Angelov M<sub>1</sub> transistor model were as  $C_{ds} = C_{gd} = 0.001 \text{ pF}$ ,  $C_{gs} = 0.1 \text{ C}_{ds}$ . As for the Statz M<sub>2</sub> model,  $C_{ds} = C_{gd} = C_{gs} = 0.001 \text{ pF}$ .

When applying the IFWM-induced phase and amplitude noise to the TIA and following on Equation 8, a normalized form is presented. A two noise source currents were embedded as part of the TIA input (in simulation), one represents IFWM-induced amplitude noise and the other represents IFWM-induced phase noise respectively. Both of these

sources are taken to be the mean-square noise current spectral density (in  $pA^2/Hz$ ). A correlation coefficient that combines the two sources is defined as  $I_{12}$  defined in the Microwave Office program:

Comparing the right hand sides of Equation 8 with Equation 12, we arrive at the following normalization:

where Equation 13 represents the IFWM-induced amplitude noise (real term) in optical fiber, while Equation 14 represents IFWM-induced phase noise as it is in an imaginary form, while the IFWM optical field amplitude is normalized in Equation 15.

Under pure thermal conditions of 30°C (i.e before applying any combined IFWM noise), circuit MNF (Minimum Noise Figure) value was 1.758 dB at 40 GHz using 81 kΩ nonlinear feedback resistor  $R_{TIA}$ , as in Fig 5. A 1.760 dB of MNF is registered when  $R_{TIA}$ value changes to 80 k $\Omega$ . The embedded IFWM optical power was taken to be 80.922 mW (within typical range of 60 mW - 120 mW). Having the amount of 80.922 mW to corresponds to 19.081 dB which was interpolated to a noise value of 8 x  $10^{-18}$  pA/ $\sqrt{Hz}$  that represents the actual noise input. The IFWM-induced noise value of 19.081 dB is effectively suppressed under the influence of TIA input impedance  $Z_{in}$  of 19914  $\Omega$  (frequency domain) which leads to IFWM combined noise of 14.55 dB in a decreasing pattern at TIA output as in Fig 6. R<sub>TIA</sub> had a considerable effect on thermal cancellation, but with little or no effect Incidentally, the thermal conditions of 30°C during IFWMon IFWM-induced noise. induced noise simulated measurement still applied. It is the virtual ground node between 1/ Cs and Zin at 40 Gb/s by which the level of IFWM combined noise is suppressed. To be more precise, the value of  $R_2$  in Fig 1 which is set at 863 k $\Omega$  that has the major dominating factor (within Z<sub>in</sub>) to control the 5 dB reduction range of IFWM noise. Higher or lower than 863 k $\Omega$  values lead to IFWM noise reduction lower than 5 dB. The presence of R<sub>TIA</sub> is only to optimize thermal noise and has nothing to do with IFWM noise control.

An assessment of S-parameters was obtained in which the TIA gain was established through  $|S_{21}|$  and found to be 10.15 dB at 40 GHz given the thermal conditions of 30°C as in Fig 7. Beyond 40 GHz, there is a considerable drop in gain which suggest that this particular configuration is valid for a band that is in a matching pattern with both noise and TIA bandwidth. An extremely limited reflected signal at the TIA input is shown as in Fig 8.

There is a very limited value of  $|S_{11}|$  at 40 GHz, despite the amount of 19914  $\Omega$  which is the input impedance of the circuit as a twin T notch filter. Resemblance to real-time fabricated TIA design is also considered for which parasitic capacitances were taken into effect.

Without the TIA procedure described above, so far, there is still serious limitations for 40 Gb/s systems or even lower if what is required is to secure a bit error rate lower than  $10^{-9}$ . IFWM and other nonlinear optical impairments are still considered to be big hurdles. Having several stages of chromatic dispersion compensation devices alongside the optical path is a tedious effort to say the least.

An optical modulation, of all forms, to tackle pulse timing jitter especially for 40 Gb/s systems can be replaced by modulating its optical power into a form of electric current. A worst case situation of simulating TIA circuit components at 30°C, gives plenty of room for real-time maximum detectable current capable of being converted into a voltage swing. The Angelov and Statz transistor models (with embedded parasitic capacitances) represent a better alternative for signal processing that can put an end to any compensation techniques.

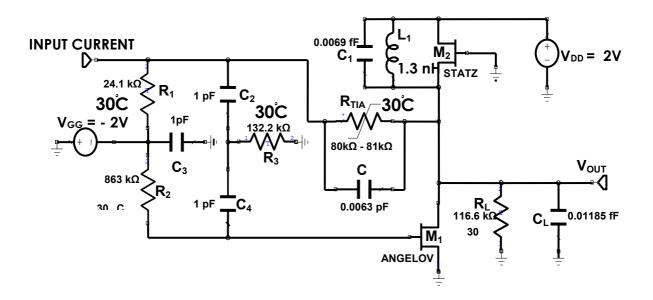


Fig. 1: Basic schematic of TIA circuit.

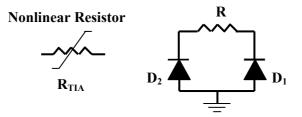


Fig. 2: Equivalent passive circuit of R<sub>TIA</sub>.

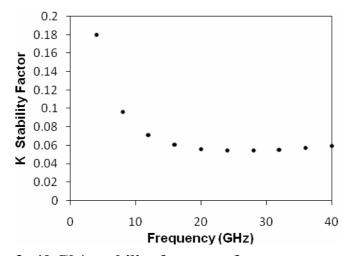


Fig. 3: 40 Gb/s stability factor performance.

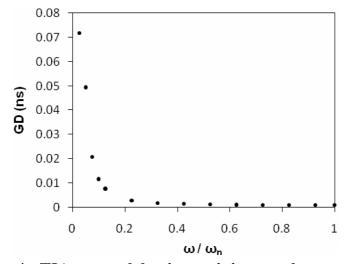


Fig. 4: TIA group delay is at minimum when  $\omega = \omega_n$ .

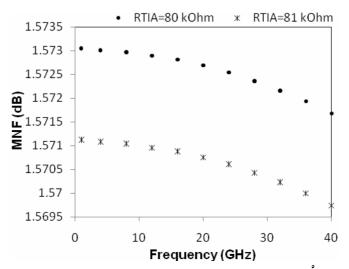


Fig. 5: Minimum Noise Fig (MNF) at temperature of 30°C (before applying Combined IFWM-induced noise) at  $R_{TIA} = 80 \text{ k}\Omega - 81 \text{ k}\Omega$ .

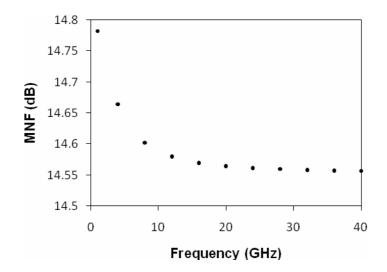


Fig. 6: Minimum Noise Fig (MNF) after applying IFWM-induced noise at  $Z_{in} = 19914 \ \Omega$  under thermal conditions of 30°C.

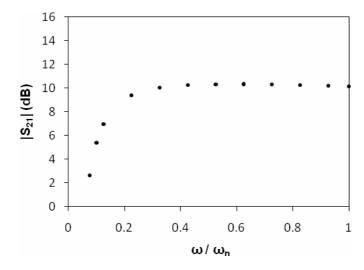


Fig. 7: |S<sub>21</sub>| versus frequency, a gain of 10.15 dB is reported at 40 Gb/s.

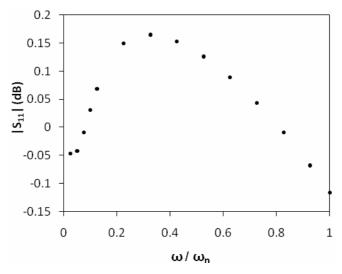


Fig. 8:  $|S_{11}|$  of reflected signal showing a value of -0.13 dB at 40 Gb/s.

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