

FPGA Implementation of Reversible Medical Image Watermarking

Dr. Ahlam Fadhil Mahmood

Computer Engineering Department

University of Mosul

ahlam.mahmood@gmail.com

Abstract

Medical image protection and authentication are becoming increasingly important in an e-Health environment where images are readily distributed over electronic networks.

This paper presents an FPGA implementation of reversible watermarking encoder and decoder system. The system is based on the Discrete Cosine Transform (DCT/IDCT) to embed and extract the copyright protection mark and least significant bit (LSB) technique to hide the patients' information and then extract back the information by the owner using.

The proposed structure employs a single multiplierless 1D-DCT/IDCT block, instead of three in many existing DCT watermarking systems, which is reduce the hardware DCT part to 16.6% compared with previous proposals. The parallel hardware implementation of DCT and LSB is done in Xilinx XSC3S500 FPGA. The proposed scheme allows multi-insertions by many doctors in order to give an exact diagnosis to the patient.

portable system of the watermarking with small size and low power dissipation

Keywords: Information hiding; watermarking ; FPGA; DCT/IDCT; Least Significant Bit method.

رقاقة البوابات القابلة للبرمجة حقليا لتنفيذ العلامة المائية وأسترجاعها للصور الطبية

د.أحلام فاضل محمود

قسم هندسة الحاسوب

ahlam.mahmood@gmail.com

أن حماية وتحقق ملكية الصور الطبية أصبحت مهمة جداً في بيئة الصحة الإلكترونية حيث تنقل الصور بسهولة عبر الشبكات الإلكترونية.

يقدم البحث نظام يستخدم البوابات القابلة للبرمجة حقليا لأدراج علامة مائية تبين ملكية الصور مع إمكانية فصل هذه العلامة مستند على خوارزمية جيب التمام المنفصل ومعكوسه لتضمين وأنتزاع العلامة. وتقنية البت الأوطى رتبة لإخفاء معلومات عن المرضى مع إمكانية أستحصال هذه المعلومات من قبل الجهة الطبية المخولة (المالكة لنفس النظام). التركيب المقترح يستخدم كتلة جيب التمام المنفصل ومعكوسه أحادي البعد بلا ضواري بدلاً من ثلاث وحدات ثنائية البعد المستخدم في العديد من أنظمة تضمين العلامة المائية، إذ خفض البناء المادي لـ 16.6% مقارنة مع الأنظمة المستخدمة جيب سابقاً. أن التنفيذ المادي المتوازي للجيب تمام وتقنية البت الأوطى طبق على رقاقة البوابات القابلة للبرمجة حقليا XC3S500. يتيح المخطط المقترح الإدخال من قبل العديد من الأطباء لكي يصفوا تشخيصاً دقيقاً إلى المريض.

1. Introduction

Watermarking is a process by which a discrete data stream called a watermark is hidden within a host signal by imposing imperceptible changes on the signal[1]. Major applications of watermarking include copyright protection and Hiding information[2].

Among different kinds of digital watermarking schemes, reversible watermarking has become a research hotspot recently[3][4]. Compared with traditional watermarking, it can restore the original cover media through the watermark extracting process; thus, reversible watermarking is very useful, especially in applications dictating high fidelity of multimedia content, such as military aerial intelligence gathering, medical records, and management of multimedia information. Hiding patient data in the medical image is one of the applications of digital image watermarking. The patient data in the electronic format is called Electronic patient record (EPR)[5].

Most of the existing watermarking algorithms are designed for software implementations in general-purpose computers [6]. However, some applications, such as HDV broadcast monitoring and on-air live broadcasting, may require a portable system of the watermarking with small size and low power dissipation, which creates the need for hardware implementation of the watermarking. Some work about circuit implementation has been reported in [7]. These architectures are implemented by mapping a DCT watermarking algorithm in a two 2-D DCT structure that are performed in parallel. Due to the large amount of the computation of the watermarking process, this kind of architectures usually requires a large number of basic blocks [8].

Another medical image watermarking system embeds is the patient information such as patient details, history and measured physiological signals in the Least Significant Bit (LSB) plane [9].

In this paper, a reversible watermarking system is proposed; the proposed scheme is mainly composed of two parts for the embedding of the watermark. First: the copyright image is based on new computation of the Discrete Cosine Transform in frequency domain. Second: the patient information embedded/extractor is based on LSB information hiding algorithm.

The rest of the paper is organized as follows. The copyright protection frequency domain issues of the proposed watermarking scheme are described in Section 2.1, in Section 2.2 the LSB replacement are present. Experimental results are given in section 2.3. Then the details of the FPGA proposed algorithm including the embedding and extracting processes are elaborated in Section 3. The hardware implementation results are given and analyzed in Section 4. Finally, conclusions are drawn in Section 5.

2. The Proposed Watermarking

In this paper, The propose scheme is composed of two parts. The first one is to embed and extract the patient's data (name, job; age, gender; mobile, hospital registrations; and diagnosis) in the medical image and the second is about how to embed logo watermarking schemes which are needed for copyright protection.

An important factor to be considered while watermarking medical images is that medical images contain Region of Interest (ROI) and Regions of Non-Interest (RONI). In medical images, ROI is an area that contains diagnostically important information and must be processed without any distortion. The watermark is usually being embedded in the RONI (Region Of Non Interest) as this region does not contribute in the process of diagnosis[11]. Thresholding, Otsu's method are used, which chooses the threshold to minimize the interclass variance of the black and white pixels, behind segmentations, the following points record the operations of the proposed algorithm:

1. Read X-ray and logo image.
2. At First, a threshold value is defined to distinguish between the region of interest (ROI) and the background area of the image (RONI). All pixel values greater than the threshold value, are considered as belongs to the region of interest or else belongs to the background.
3. Search in RONI region to find first same logo size window.
4. Use frequency domain Discrete Cosine Transformation to embed logo image. The DCT of both the images are taken and the proportion in which the DCT is added can vary the intensity of the image that will appear as the watermark.
5. Initialize the RONI area to embed patient information at the end of logo insertion region, divide the remainder RONI area into five field , first field to insert patient Name and job; Second field content gender, mobile and age ; , Third field to embed the hospital record information; fourth field hide the first doctor diagnosis;. The last field allows multi-insertions by many doctors in order to give an exact diagnosis to the patient. Each field are ended using ";" mark, the last field space is all the reminder RONI LSB bits.
6. Save the obtained image and stored as report in Electronic patient record (EPR) and it can be transmit to any other hospital to embed new diagnosis.
7. The receiver hospital whose have the decoder system can extract the logo image after read key from received image by applying Inverse transform.
8. Extract the first four field by inverse LSB method with convert the binary bits to ASCII code and save it as text file.
9. The new doctors in received hospital can embed new case diagnosis as text and enable hardware system to hide it in last field. The embedded and extract schemes are verified in the following flow charts.

2.1 The First Proposed Watermarking Scheme Based on DCT/IDCT:

One of the most commonly used watermarking procedures involving the 2-D DCT [7][8][10]. The basic process that is taking place is the addition of DCTs followed by the IDCT of the result which gives the watermarked image [7].

$$V_i' = V_i * (1 + \alpha * (X_i + \beta * W_i)) \quad \dots\dots\dots (1)$$

V_i' is the result of the added 2D-DCT of the two images. X_i is the 2D-DCT value of the image on which the watermarking is done and W_i is the 2D-DCT of the logo which is watermarked on the image. The constants α and β , decides the visibility of the watermark. If the value of β is very small, we obtain an invisible watermark and as its value increases the visibility simultaneously increases.

For the extraction of a watermark, the value of α and β has to be known, hence, the extraction of the watermarking cannot be done by anyone who does not know those values.

As shown in Figure 2.a, there are two 2-D DCT computations which can be performed in parallel [7][10]. One is for the original image x and the other for the watermark w . Most literature architectures are implemented by mapping a DCT watermarking algorithm in a structure in which the 2-D DCT of the signal and that of the watermark are performed in parallel.

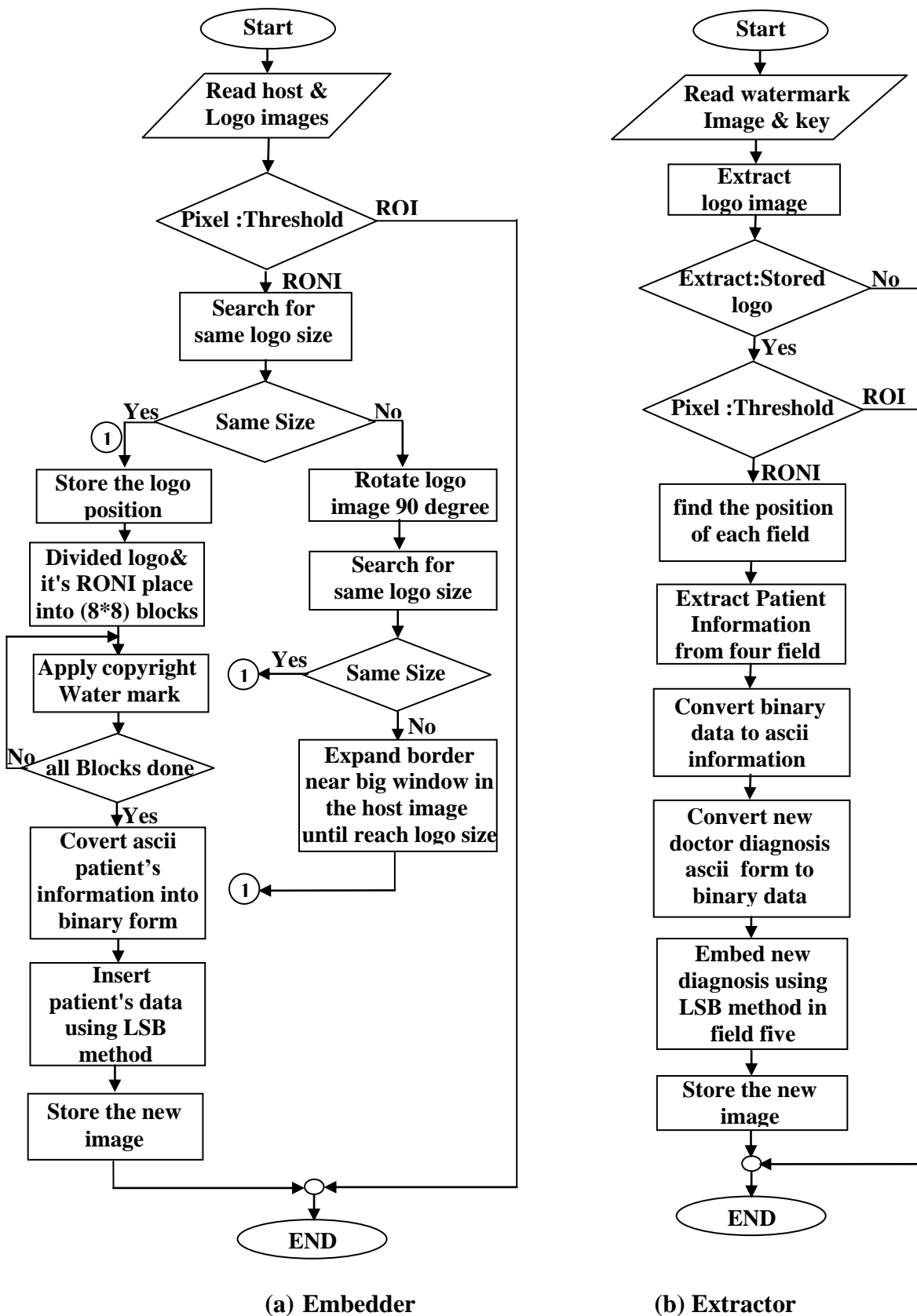
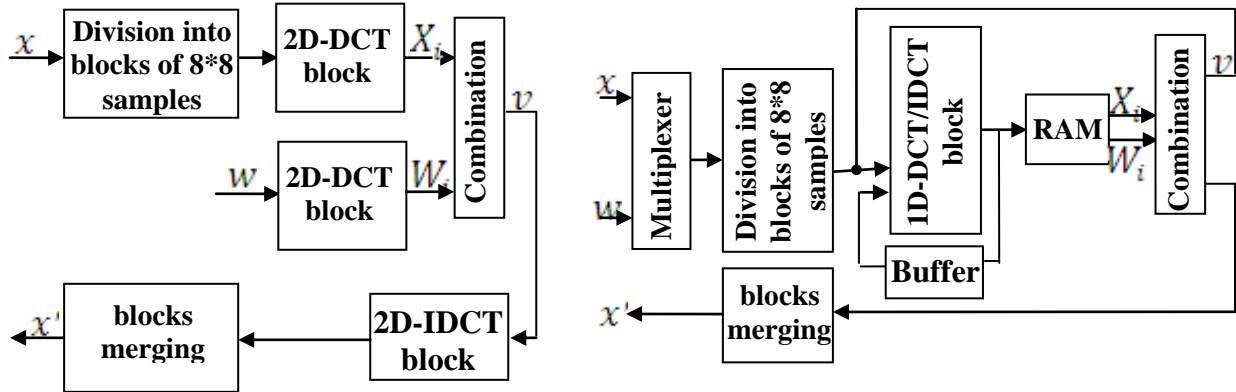


Figure 1: Flow Chart of the Proposed System



(a) General scheme of the Watermarking using the 2D- DCT (b)The Proposed watermarking Scheme

Figure 2: The 2D-DCT Watermarking Scheme

The proposed architecture shown in figure 2.b has only one 1D-DCT/IDCT multiplierless block present in [12]. The 2-D DCT of the original image and that of the watermark performed one after another. As the amount of the computation of the latter is made much smaller than the former, the overall delay is not much larger than that of the two 2-D DCTs in parallel. The hardware consumption can thus, be reduced as Pie chart illustrated in Figure 3.a without sacrificing much the speed.

A look at RONI region in the original image a large number of adjacent pixels within the block has equal values. The 2D-DCT of these (8×8) blocks is only DC element [13]. Using the DCT (0,0) formula to calculate the 2D-DCT and leaving all other AC components. The omitted operations are more than 33.4% of total operation as verify in Figure 3.b.

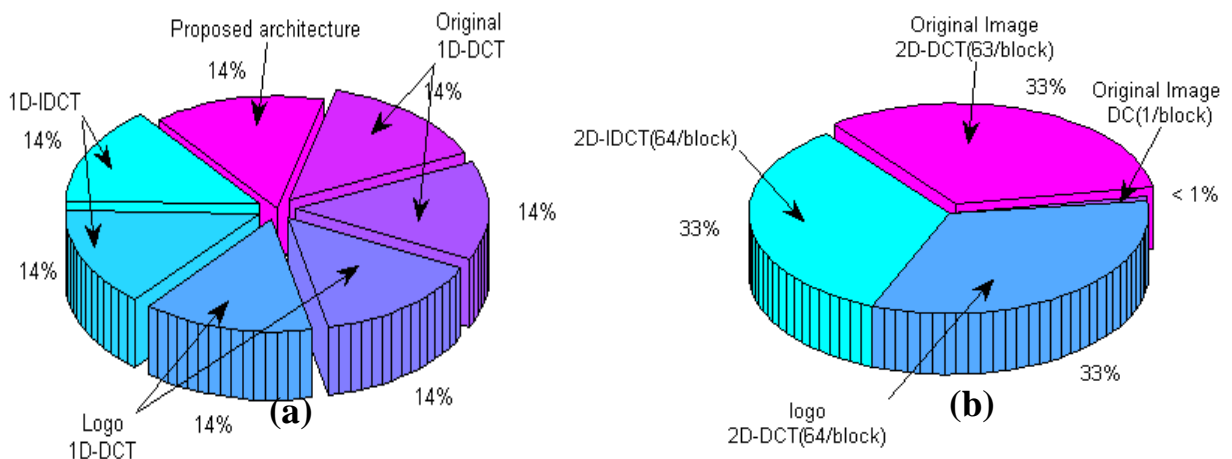


Figure 3: Pie charts (a)illustrates percents Hardware reduction (b)illustrates percents omitted operation in each 8×8 block

The proposed architecture work in three phases have two steps, in the first step, calculate the 2D-DCT of Logo image, and in the second phase, only DC coefficient of original image blocks are calculated and stored in the RAM-memory. To apply equation (1) formula in combination stage, the Inverse Discrete Cosine Transform are done in last phase, all phases

are done on the same unit. The LOGO embedding image are shown in Figure 4 for varying values of α and β .

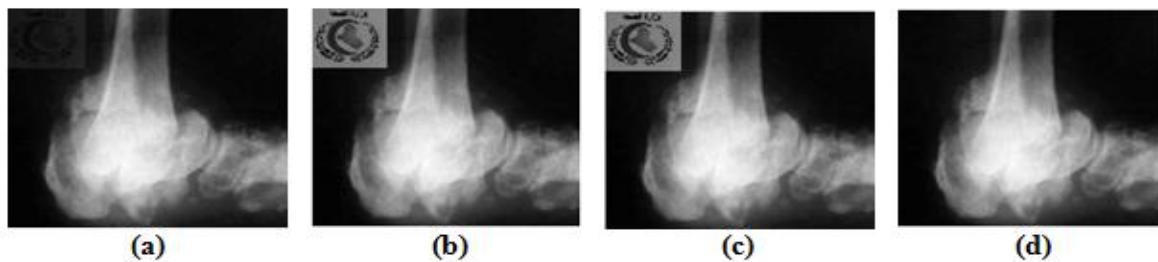


Figure 4. The images obtained after watermarking (a) for $\alpha=.97$ and $\beta=.07$ (b) for $\alpha=.97$ and $\beta=.27$ (c) for $\alpha=.97$ and $\beta=.47$ (d) invisible watermarking

2.2 The Second Proposed Algorithm using LSB Replacement

The second part of the proposed system is to embed the patient information inside X-ray host image, Our scheme is composed of two parts. The first one is to embed the patient’s diagnosis in the medical image and the second is about how to extract it. This procedure must be carried out while guaranteeing the good quality of the image.

One of the most direct and simple technique is to embed the watermark code into the LSBs (Least Significant Bits) of the image. Since a change in LSB corresponds a change in 1 unit of image gray value, its modification is not perceivable by human eyes. The beauty of X-ray images and all other medical images is that the LSBs for all pixels in the RONI are zeroes. The watermark is reversed by simply setting the LSBs of RONI back to zero.

The insertion method consists in carrying out a binary conversion then we divide the image into 8 plans of bits. Next, replace the last plans with the four message field after inverted binary bits. This function gives a message which we insert in LSB plan. Finally, we rebuild the 8 plans to lead to the watermarked image that is shown in figure 5.

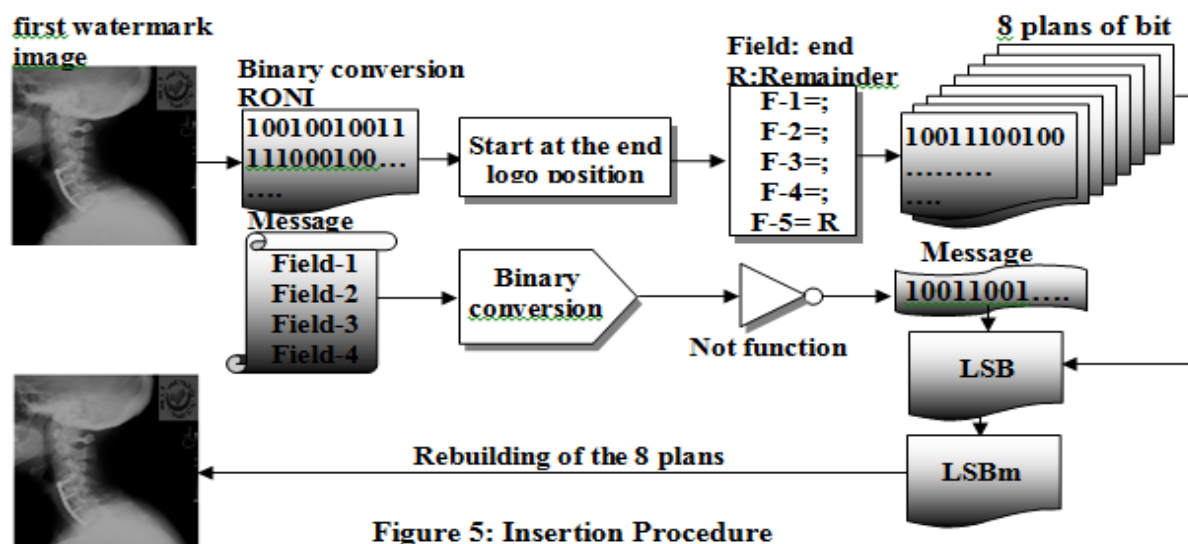


Figure 5: Insertion Procedure

The following doctor embeds his diagnosis in the medical image received from the previous doctors by using the same procedure. The diagram insertion of the following doctors is like figure 6 which we replace the original image by the watermarked image. The patient

information are extract by construct inversion LSBs , converted to ascii code and store as text file.

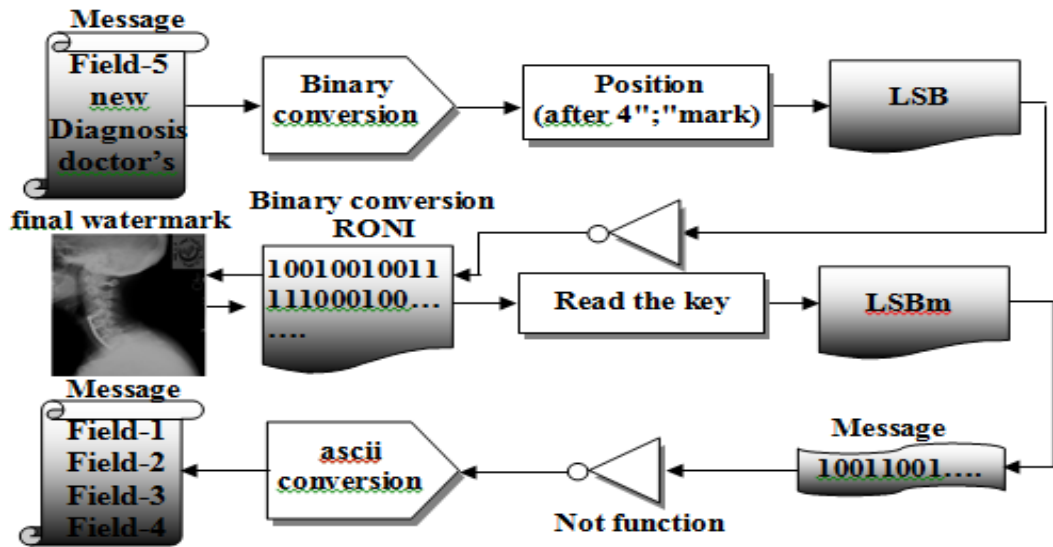


Figure 6: Patient data extraction and new diagnosis insertion

2.3 Experimental Results:

The proposed reversible watermarking scheme are implemented, and have successfully applied to many test images with different sizes and complexities as shown in Figure 7 . Equality between the original images and restored images has proved the reversibility of the proposed scheme in experiments. In our experiments, we take Iraqi health ministry LOGO and adopt peak signal-to-noise ratio (PSNR) value and byte number as measurements of image quality and embedding capacity, respectively as written in table 1.

Table 1: Comparison Results in terms of the Capacity (bits) and the PSNR value (dB) for different medical images.

Image	Image size (Pixel)	threshold	Capacity (bit)	PSNR (dB)	Embedded (bit)	Error (embed – extract)data
Image_1	867*574	0.4	310618	48.7	728	0
Image_2	425*400	0.2745	100923	46.6	848	0
Image_3	604*516	0.2909	184597	47.3	800	0
Image_4	800*420	0.3843	62650	45.1	960	0
Image_5	738*600	0.2627	200657	46.4	984	0
Image_6	740*539	0.2745	218519	45.9	880	0
Image_7	867*574	0.3176	1351307	47.7	896	0
Image_8	480*640	0.3686	171825	46.6	736	0
Image_9	439*468	0.3451	117075	44.9	832	0

3. The Proposed FPGA Architecture.

The architecture of the encoder-watermarking chip is shown in Figure 8 consists of three distinct modules: LOGO DCT insertion module, Patient Information LSB module, and controller module. The structures of the extraction and insertion modules are very similar,

due to uses of same components. The 1D-DCT multiplierless module present in [12], two multiplexers, one adder, two accumulators, three comparator and latches for pipeline implementation. The lower multiplexer (MUX) is used to choose between the watermarking strength factors, medical image and LOGO.

The top multiplexer helps in selecting an copy RONI or LOGO insertion process of watermarking insertion. The same block uses for 1D-DCT and 1D-IDCT, this architecture provides the capability to watermark a DCT block with minimum FPGA resources, latches are used for temporary buffering.

Buffer circuitry is used to assist in finding the transpose. It also serves as temporary storage for the first 1D DCT/IDCT coefficient, added control signal to select between DCT and IDCT operation. The DCT module does not have a separate controller; i. e. the main controller controls it. The controller is modeled as a synchronizer between modules. The key (α and β) are read from storage to the input register for their DCT coefficients to be calculated. The binary form of medical image is stored in memory after compare with appropriate threshold by comparator.

The structure of the decoder chip is shown in Figure 9 that have the same 1D-DCT/IDCT modules, it starts operations when read the embed LOGO position key form the last 20 frame bits. After complete LOGO extraction, it compares with origin LOGO input and output one bit to check similarity. The LSB extraction uses LUT to convert the binary reconstructed bytes to ASCII code as well as insert the new diagnosis at the last field.

4. Implementation Results

The prototype is implemented in VHDL and synthesized using Xilinx Spartan-3E technology with an XC3S500-5fg320 target device. In order to provide a numeric example, we calculated the number of the calculation units, such as multipliers and adders, needed for mapping the proposed architecture with other references, the results are presented in Table 2. One can see that the proposed-DCT/IDCT-block structure needs only approximately 33.9% of the hardware of that of the one-DCT-blocks present in [8]. The increase of the time delay resulting from replacing the two-DCT-blocks by the one DCT structure is about twice, to dramatically parallel work as LSB procedure still busy.

Table 2: Comparison of Hardware Consumption and the time delay.

	Single Ref[8]	One Ref[8]	Ref[7]	Proposed Structure	
	encoder	encoder	encoder	encoder	decoder
function	LOGO only	LOGO only	LOGO only	L&P	L&P
Multipliers	42	22	16	0	0
Adders	72	37	16	24	24
Latency	64T	86T	163T	193T	193T
Clock(MHZ)	1	1	1	50	50
Frequency	Not given	Not given	83.996	79.789(MHZ)	80.89(MHZ)
Throughput	Sample/clock	Sample/clock	2 Sample/clock	Sample/clock	Sample/clock

T is the minimum interval between the two consecutive output samples depend on target FPGA , L&P=LOGO and Patient data.

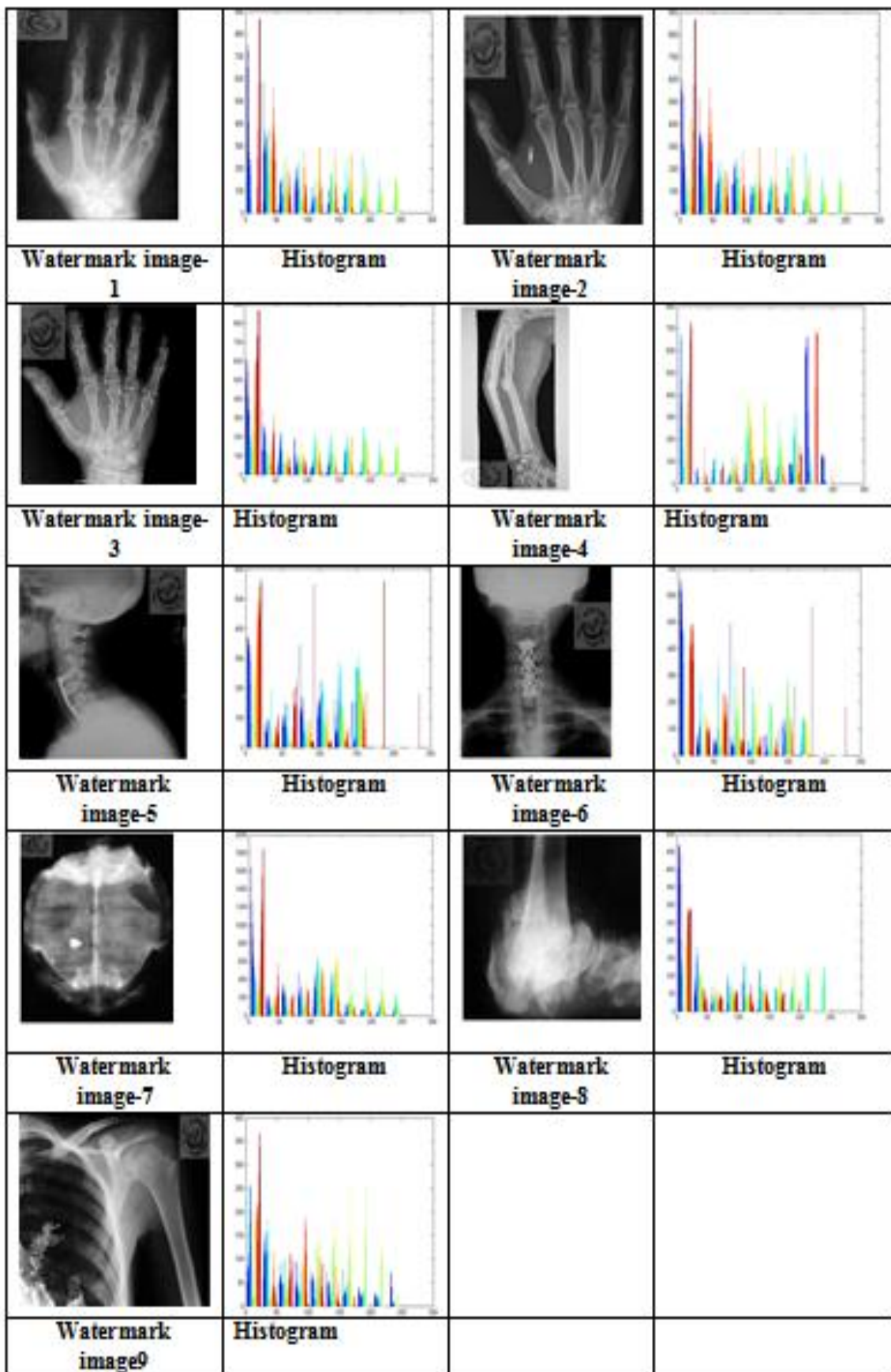


Figure 7: Watermarked versions of test images ($\alpha=.97$ and $\beta=.27$)

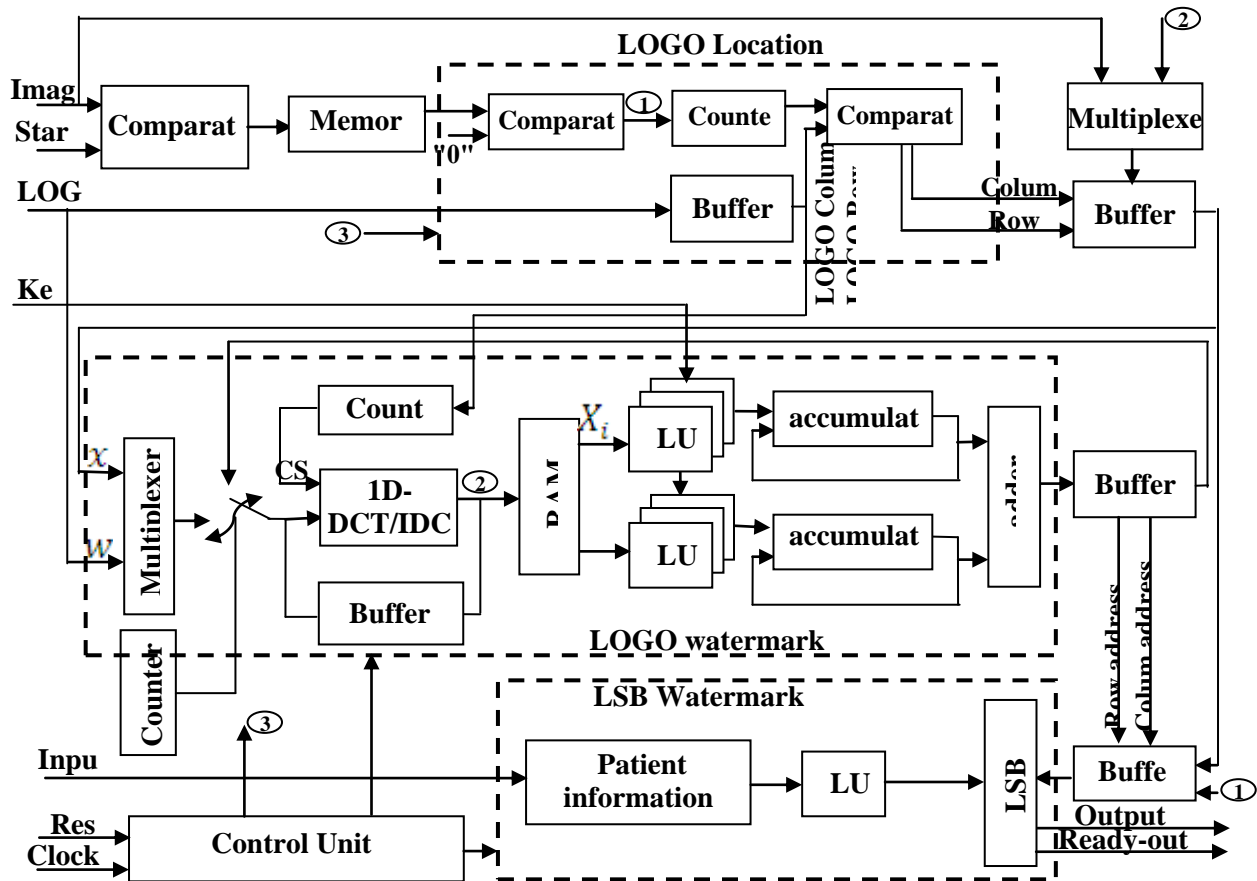


Figure 8: Architecture of the Encoder Chip

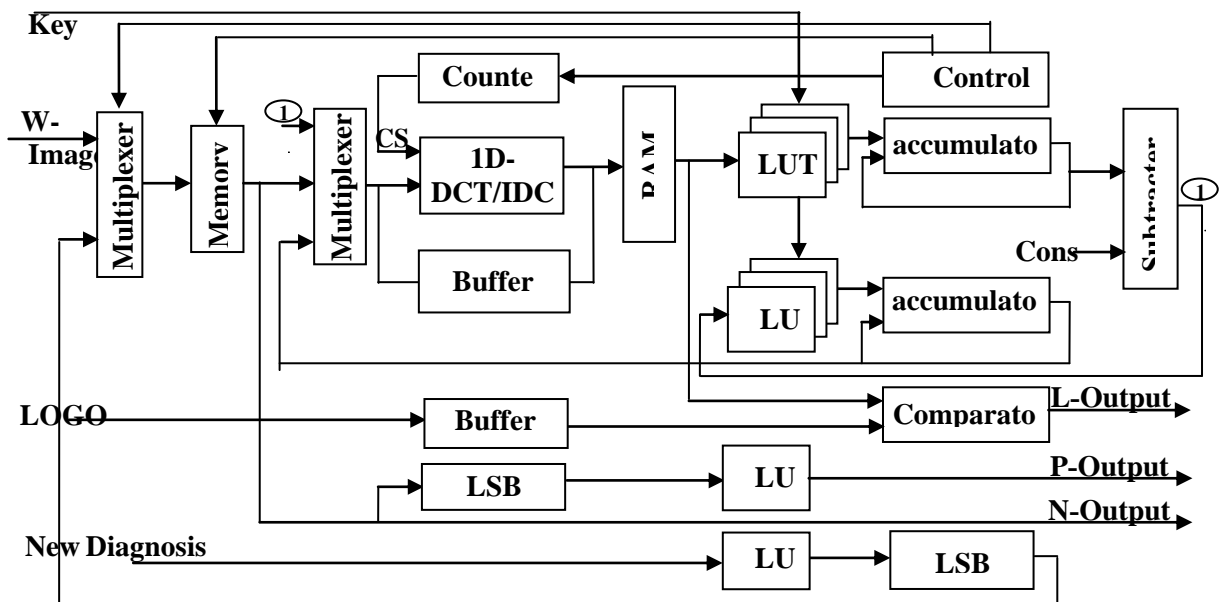


Figure 9: Architecture of the Decoder Chip

5. Conclusions

This paper presented an FPGA Implementation of Electronic Patient Record system for multi-media communication between doctors and information technologies to protect the privacy of patients' records.

A multiplierless computation structure of image watermarking in DCT domain has been presented. It employs only a single 1D-DCT block to perform the 2-D DCT computation of the original image and that of the watermark in a serial manner to reduce the hardware consumption in circuit implementation.

According to the experimental results, the proposed reversible scheme provides a higher capacity and achieves better image quality for watermarked images. In addition, the power consumption is small and it operates in higher frequency greater than 100MHZ with one sample per clock throughput

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The work was carried out at the college of Engineering. University of Mosul